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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/711,538	09/24/2004	Behzad Razavi	REAP0144USA	5537
27765	7590	12/16/2005	EXAMINER	
NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION			NGUYEN, PATRICIA T	
P.O. BOX 506			ART UNIT	
MERRIFIELD, VA 22116			PAPER NUMBER	
			2817	
DATE MAILED: 12/16/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.		Applicant(s)	
	10/711,538		RAZAVI ET AL.	
	Examiner		Art Unit	
	Patricia T. Nguyen		2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 9-12 is/are allowed.
- 6) ☒ Claim(s) 1-4, 8 and 13-16 is/are rejected.
- 7) ☒ Claim(s) 5-7 and 17-20 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|----------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. ____. |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>11/18/04</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Objections

Claim 20 is objected to because of the following informalities:

Claim 20, line 1, "The method of claim 9" should read -- The method of claim 19 -- .

Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4, 8, and 13-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Bartlett et al., U.S. Patent # 6,232,841 B1.

Figs. 2, 3a, and 4 of Bartlett et al. discloses a circuit comprising:
a switched loading circuit (In Figs. 2 and 3, load network 100 including tunable reactive network 102, see spec., col. 3, lines 40-61) comprising a plurality of loading units (in Fig. 3a, loading units inductors L1-L3 and capacitors C4-C6 in network 102, in Fig. 4, loading units inductor L1, capacitor C1), each of the loading units determining a corresponding center frequency of the LNA, the switched loading circuit for selectively enabling at least one loading unit having the corresponding center frequency; and at

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least one converter (Q1 in Figs. 2, 3a, and 4) coupled to the switched loading circuit for converting the input signal (RF input) into a loading current and passing the loading current through the enabled loading unit to generate the output signal (RF output) (see spec. col. 10, lines 56-65).

Regarding claims 13-16, although Bartlett et al. does not have his method written out structurally, his method resides inherently in his apparatus.

Claims 1-2, 8, 13-14 are rejected under 35 U.S.C. 102(e) as being anticipated by Meck et al., U.S. Patent # 6,215,355 B1.

Figs. 3 and 4 of Meck et al. discloses a circuit comprising: a switched loading circuit (transistors switches TL, TR, load networks load L, load R) comprising a plurality of loading units (load L, load R), each of the loading units determining a corresponding center frequency of the LNA, the switched loading circuit for selectively enabling at least one loading unit having the corresponding center frequency; and at least one converter (TT) coupled to the switched loading circuit for converting the input signal (RFin) into a loading current ID and passing the loading current through the enabled loading unit to generate the output signal.

Regarding claims 13-16, although Meck et al. does not have his method written out structurally, his method resides inherently in his apparatus.

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(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-4, 8, 13-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Segawa, U.S. Patent # 6,825,722 B2.

Fig. 1 of Segawa discloses a circuit comprising: a switched loading circuit (transistors switches 12, 13, load networks 14, 15) comprising a plurality of loading units (14 and 15 including inductors 14_1, 15_1, capacitors 14_2, 15_2, reasistors 14_3, 15_3), each of the loading units determining a corresponding center frequency of the LNA (10), the switched loading circuit for selectively enabling at least one loading unit having the corresponding center frequency; and at least one converter (11) coupled to the switched loading circuit for converting the input signal (RF) into a loading current and passing the loading current through the enabled loading unit to generate the output signal (IF).

Regarding claims 13-16, although Segawa does not have his method written out structurally, his method resides inherently in his apparatus.

Allowable Subject Matter

Claims 5-7 and 17-19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 9-12 are allowed.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents # 6,963,241 B2, # 3,883,815, # 6,940,358 B1, # 6,806,777 B2, and # 6,724,259 B2 contain some limitations of the claimed invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patricia T. Nguyen whose telephone number is (703) 308-1927. The examiner can normally be reached on 6:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 703-309-4940. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PTN
December 11, 2005

Patricia Nguyen
PATRICIA NGUYEN
PRIMARY EXAMINER